

Related publications of the partners

Fraunhofer EMFT:

- (1) M. Burghart, M. Muhammad, W. S. Mumtaz, I. Bose, K. Bock: Manufacturing and Material Formulation Strategies for Bottom-Gate Polymer Dielectrics in Complementary Organic TFTs, Int. Symp. on Flexible Electronics 2010 (ISFE 2010), 11.-14.4.2010, Palma de Mallorca, Spain
- (2) Gerhard Klink: "Bringing Organic Electronics to the Roll", invited talk, Polaric/Cosmic workshop 2010, 22- 23 September 2010, Imperial College, London, UK.

IMEC:

- (3) D. Bode, K. Myny, B. Verreet, B. van der Putten, P. Bakalov, S. Steudel, S. Smout, P. Vicca, G. Gelinck, J. Genoe and P. Heremans, Organic complementary MOSFET technology operating at stage delays below 1us. Presented at: MRS Spring Meeting Symposium II: Materials Science and Charge Transport in Organic Electronics. (5-9 April 2010; San Francisco, CA, USA)
- (4) Jan Genoe, Kris Myny, S. Steudel, S. Smout, P. Vicca, B. van der Putten, A. K. Tripathi, Nick A.J.M. Van Aerle, Christiane Grillberger, Olaf R. Hild, Gerwin H. Gelinck, Wim Dehaene, and Paul Heremans, "Increasing complexity of organic transistor circuits on foil: 128 bit organic RFID tags and flexible display backplanes", invited talk, 6th Global Plastic Electronics Conference, October 19-21, 2010, Dresden, Germany
- (5) Jan Genoe, Kris Myny, S. Steudel, S. Smout, P. Vicca, B. van der Putten, A. K. Tripathi, Nick A.J.M. Van Aerle, Gerwin H. Gelinck, Wim Dehaene, Paul Heremans, "Improving organic transistor building blocks by technology and design", invited Tutorial talk, Polaric/Cosmic workshop 2010, 22- 23 September 2010, Imperial College, London, UK.
- (6) Soeren Steudel, Kris Myny, Jan Genoe, Paul Heremans and Gerwin Gelinck, "Towards EPC compatible RFID tags" LOPE-C 2010, 31st May-2nd June 2010, Frankfurt, Germany
- (7) W.Y. Lin, R. Muller, S. Steudel, J. Genoe, P. Heremans "Low cost solutionprocessed high-k gate dielectric materials for large area circuit applications", 3rd International Symposium on Flexible Organic Electronics (IS-FOE 2010), 6-9 July 2010, Halkidiki, Greece, best oral presentation in IS-FOE 2010
- (8) Kris Myny, Erik van Veenendaal, Gerwin H. Gelinck, Jan Genoe, Wim Dehaene, and Paul Heremans, Proceedings of ISSCC 2011, Session 18.1, San Francisco, February 20-24, 2011
- (9) Nicola Wrachien, Andrea Cester, Nicola Bellaio, Alessandro Pinato, Matteo Meneghini, Augusto Tazzoli, Gaudenzio Meneghesso, Kris Myny, Steve Smout and Jan Genoe, "Light, Bias, And Temperature Effects On Organic TFTs", 2010 IEEE International Reliability Physics Symposium (IRPS 2010), May 02-06, 2010, Hyatt Regency Orange County, Anaheim, California
- (10) Jan Genoe, Kris Myny, S. Steudel, S. Smout, P. Vicca, B. van der Putten, A.K. Tripathi, Nick A.J.M. van Aerle, Gerwin H. Gelinck, Wim Dehaene, Paul Heremans, "Organic transistor technology options for device performance versus technology options for increased circuit reliability and yield on foil", Device Research Conference 2010, June 21-23, 2010, South Bend, In, USA (invited), Proc. DRC 2010, pp 170-173.
- (11) Paul Heremans, Kris Myny, Hagen Marien, Erikvan Veenendaal, Soeren Steudel, Michiel Steyaert, Jan Genoe, Gerwin H. Gelinck, "Progress and outlook for Digital and analog circuits with plastic electronics" International workshop on flexible & printable electronics, Muju resort, korea, sept 8-10, 2010

- (12) Jan Genoe, Kris Myny, Soeren Steudel and Paul Heremans, "Design and manufacturing of organic RFID circuits", Tutorial session T6 Organic Electronics (Paper: 413), IEEE-ACM ICCAD 2010 international conference on computer aided design, November 7-11, 2010, San Jose, CA. USA.
- (13) Soeren Steudel, "Thin film transistors on foil - application requirements and state of the art", Tutorial Symposia DEFG, MRS Fall 2010, Boston, USA.
- (14) Soeren Steudel, "Towards EPC compatible plastic RFID tags" 9th September 2010, invited lecture, Imperial College London, United Kingdom
- (15) Wan-Yu Lin et al, "Low-cost solution processed high-k gate dielectric materials for Large Area Circuit Application", Symposium F, 29th November 2010, MRS Fall 2010, Boston, USA.

CEA-Liten

- (16) S. Jacob et al., "Full printed organic CMOS circuits for large area electronics", LOPE-C, 31 May 2010 - 2 June 2010, Frankfurt
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- (18) Al Seiler et al.: "Versatile Printing and Laser Techniques for the Processing of Unipolar and Complementary Organic Circuits", LOPE-C, 31 May 2010 - 2 June 2010, Frankfurt
- (19) I. Chartier: "CEA-Liten S2S printing platform for Organic CMOS and Integrated Smart Systems applications", invited paper, Printed Electronic 2010, Dresden
- (20)
- (21) Romain Gwoziecki, "Sheet-to-sheet printed High Mobility Organic, Complementary Devices and Circuit - Polaric/Cosmic workshop 2010, 22- 23 September 2010, Imperial College, London, UK
- (22) I. Chartier, L'électronique imprimée, une industrie naissante ATIP conference (Association Technique de l'Industrie Papetière), 18. Nov. 2010

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- (23) Martin Heeney: Semiconductors for OFETs. Polaric/Cosmic workshop 2010 on Complementary Organic Devices and Circuits, 22- 23 September 2010, Imperial College, London, UK

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- (24) E. Cantatore: The Impact of Complementary OTFT Technology on Integrated Circuit Design and its Applications, Plastic Electronics Conference 2010, Dresden, 19-21 October 2010.